

Jay Shriram Group of Institutions organizes 3rd National conference on “Innovative Research on VLSI Design, Embedded System and Networks” will be held on 20th March

[Web Reference: TECHNICALSYMPOSIUM.COM](http://TECHNICALSYMPOSIUM.COM)

Registration Form

Name	
Designation	
Category (Tick Appropriate)	<input type="checkbox"/> Research Scholars <input type="checkbox"/> Academicians <input type="checkbox"/> Student <input type="checkbox"/> Professionals
Gender	<input type="checkbox"/> Female <input type="checkbox"/> Male
Institution	
Address	
Contact Name	
Phone Number	
E-mail	
Accommodation Needed (Yes/No)	
Title of Paper	
Paper ID	
DD No.	
Date	
Amount	
Bank	

Signature:

Participant

Head with Seal

Send DD along with Reg. Form to: Prof. J.Nandhini-HOD/ECE, Convener-IRVEN 2015, Jay Shriram Group of Institutions, Avinashipalayam, Tirupur– 638 660-cell:9585591406.